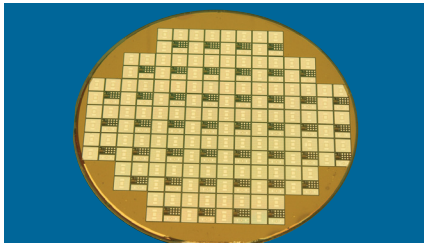
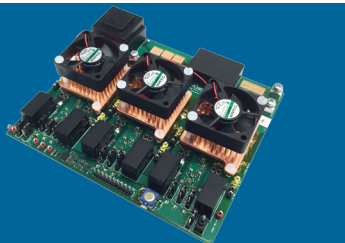


The 6th Annual IEEE Workshop on Wide Bandgap Power Devices & Applications

Georgia Tech Hotel and Conference Center
October 31 – November 2, 2018
Atlanta, Georgia | www.wipda.org



The IEEE Workshop on Wide Bandgap Power Devices & Applications (WiPDA) provides a forum for device scientists, circuit designers, & application engineers from the Power Electronics & Electron Devices Societies to share technology updates, research findings, experience & potential applications.

TOPICS OF INTEREST

- › Heteroepitaxial & Bulk Materials Growth
- › Gate Dielectrics & Surface Passivation
- › Device Structures & Fabrication Techniques
- › Device Characterization & Modeling
- › Very-High Efficiency Or Compact Converters
- › Safe Operating Areas Of Wide Bandgap Devices, Including Short Circuit, Spike, & Transient Tolerance
- › Harsh Environment (High Temperature) Operation & Reliability
- › Packaging Power Modules & ICs
- › Hard-Switched & Soft-Switched Application Analysis
- › Gate Drive & Other Auxiliary Circuits
- › High-Performance Passive Components
- › Applications in Renewable Energy & Energy Storage, Transportation, Industrial Drives, & Grid Power Systems

All presented papers will be included in the conference proceedings and submitted to the databases of IEEEExplore.

ORGANIZING COMMITTEE

Maryam Saeedifard, General Chair, Georgia Tech
Victor Veliadis, Vice-Chair, NC State/Power America
Sandeep Bahl, Technical Program Co-Chair, Texas Instruments
Dong Cao, Technical Program Co-Chair, North Dakota State University
Mahshid Amirabadi, Technical Program Co-Chair, Northeastern University
Khurram Afridi, Technical Program Co-Chair, University of Colorado - Boulder
Eric Persson, Plenary Sessions Chair, Infineon
Babak Parkhideh, Treasurer, North Carolina -Charlotte,

PARTICIPATION OPPORTUNITIES

- › **TUTORIALS:** Tutorial proposals on state-of-the-art WBG power device technology and applications are sought. Tutorials will be offered in the morning and afternoon of October 31.
- › **KEYNOTE SESSIONS:** Two keynote sessions will be held on November 1 and 2, where leading experts from academia, industry, and research institutes will be invited to share their insights on technology developments and future trends.
- › **PANEL SESSION:** Leading experts from federal agencies, industry, and academia will be invited to discuss pressing technical issues at a panel session on November 1.
- › **EXHIBITION & SPONSORSHIPS:** A limited number of exhibitor and corporate sponsorship opportunities are available.

KEY DATES

- › Two Page Abstract: **JUNE 15, 2018**
- › Notification Of Acceptance: **JULY 20, 2018**
- › Tutorial Abstract Submission: **JUNE 29, 2018**
- › Tutorial Notification Of Acceptance: **JULY 27, 2018**
- › Final Paper Submission: **AUGUST 24, 2018**
- › Tutorials: **OCTOBER 31, 2018**
- › Tech. & Dialogue Sessions: **NOVEMBER 1-2, 2018**

Karthik Kandasamy, Local Organizing Committee, Georgia Tech
Suman Debnath, Tutorials Co-Chair, Oak Ridge National Laboratory
Jiangchao Qin, Tutorials Co-Chair, Arizona State University
Mark Scott, Publicity Chair, Miami University
Shyh-Chiang Shen, Publications Chair, Georgia Tech
Lingxiao Xue, Website Chair, Navitas Semiconductor
Stephen Oliver, PSMA Rep., Navitas Semiconductor

