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The 1st IEEE Workshop on Wide Bandgap Power Devices and Applications



Oct. 27-29, 2013 Columbus, Ohio, USA Schedule

Sunday 10/27/13

Time:	PFAHL 202	PFAHL 302	Blackwell Ballroom Lobby
12:00 PM-8:00 PM			Registration
1:00 PM-3:00 PM	<i>Tutorial: Enhancement Mode GaN on Silicon Enables Increased Performance and New Applications</i> , David Reusch, Efficient Power Conversion Corporation	<i>Tutorial: SiC Power Devices – Fundamentals, MOSFETs and High Voltage Devices</i> , Brett Hull, CREE, Inc.	
3:00 PM-3:30 PM	BREAK	BREAK	
3:30 PM-5:30 PM	<i>Tutorial: Further Prospects with SiC power semiconductors – Schottky diodes, JFET transistors and package considerations</i> , Peter Friedrichs, Infineon Technologies AG	<i>Tutorial: WBG Technology for Transportation Applications</i> , Madhu Chinthavali, Oak Ridge National Laboratory	

Time:	Blackwell Ballrooms B & C
6:00 PM-8:00 PM	Welcome Reception

Monday 10/28/13

Time:	Event	Location
7:00 AM-8:00 AM	Authors' Breakfast	Pfahl 2 nd Floor BRK AREA
7:30 AM- 4:00 PM	Registration	Blackwell Lobby
8:00 AM-8:10 AM	Welcome	Blackwell Ballroom
8:10 AM-8:40 AM	<i>Keynote: Dong Tan</i> , Northrop Grumman Aerospace Systems	Blackwell Ballroom
8:40 AM-9:10 AM	<i>Keynote: Tatsuzo Ueda</i> , Panasonic Corporation	Blackwell Ballroom
9:10 AM-9:40 AM	<i>Keynote: Chingchi Chen</i> , Ford Motor Company	Blackwell Ballroom
9:40 AM-10:10 AM	<i>Keynote: Leon Tolbert</i> , University of Tennessee, Knoxville	Blackwell Ballroom
10:10AM-10:30AM	BREAK	

Time:	PFAHL 202	PFAHL 140
10:30 AM-10:55 AM	<i>Technical Session: GaN based Circuit Prototyping</i>	<i>Technical Session: GaN Devices</i>
10:55 AM-11:20 AM		
11:20 AM-11:45 AM		

Time:	Blackwell Ballrooms A-C	PFAHL 240
11:45 AM-1:30 PM	LUNCH	Steering Committee Meeting/Lunch

Monday 10/28/13 (cont.)

Time:	PFAHL 202	PFAHL 140
1:30 PM-1:55 PM	<i>Technical Session: SiC based Power Module and Integrated Circuits</i>	<i>Technical Session: SiC Devices</i>
1:55 PM-2:20 PM		
2:20 PM-2:45 PM		
2:45 PM-3:10 PM		
3:10 PM-3:30 PM	BREAK	BREAK
3:30 PM-3:55 PM	<i>Technical Session: High Performance Gate Drives</i>	<i>Technical Session: Packaging and Device Scaling</i>
3:55 PM-4:20 PM		
4:20 PM-4:45 PM		

Time:	Huntington Club
5:15 PM-6:30 PM	Poster Session
6:30 PM-8:30 PM	Banquet

Tuesday 10/29/13

Time:	Event	Location
7:00 AM-8:00 AM	Authors' Breakfast	Pfahl 2nd Floor BRK AREA
7:30 AM- 11:30 AM	Registration	Blackwell Lobby
8:00 AM-8:10 AM	Morning Refreshments	Blackwell Ballroom
8:10 AM-8:40 AM	<i>Keynote: Hao Huang, GE Aviation</i>	Blackwell Ballroom
8:40 AM-9:10 AM	<i>Keynote: John Palmour, CREE, Inc</i>	Blackwell Ballroom
9:10 AM-9:40 AM	<i>Keynote: Fang Z. Peng, Michigan State University</i>	Blackwell Ballroom
9:40 AM-10:10 AM	<i>Keynote: Allen Hefner, National Institute of Standard and Technology</i>	Blackwell Ballroom
10:10AM-10:30AM	BREAK	

Time:	PFAHL 202	PFAHL 140
10:30 AM-10:55 AM	<i>Technical Session: Device Testing</i>	<i>Technical Session: New Devices</i>
10:55 AM-11:20 AM		
11:20 AM-11:45 AM		

Time:	Blackwell Ballrooms A-C
11:45 AM-1:30 PM	LUNCH

Time:	PFAHL 140
1:30 PM-3:00 PM	Panel Discussion <i>Anant Agarwal, Department of Energy; Sharon Beermann-Curtin, Office of Naval Research; Michael Briere, International Rectifier; Daniel S. Green, Defense Advanced Research Project Agency; Joseph Weimer, Wight-Patterson Air Force Base</i>
3:00 PM-3:20 PM	BREAK

Time:	PFAHL 202	PFAHL 140
3:20 PM-3:45 PM	<i>Technical Session: WBG based High Power Circuits</i>	<i>Technical Session: Gate and Interface</i>
3:45 PM-4:10 PM		
4:10 PM-4:35 PM		
4:35 PM-5:00PM		
<i>(end conference)</i>		